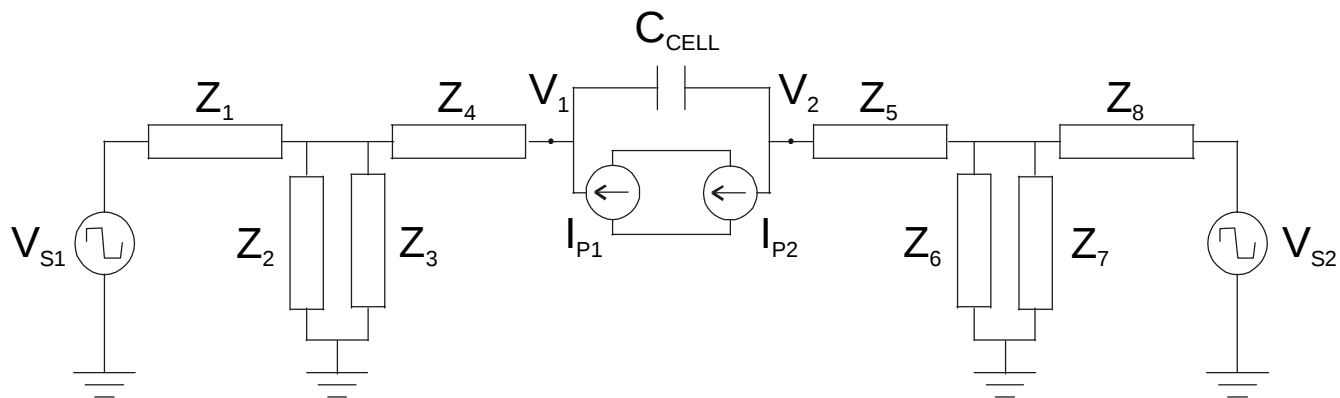




Where Z_{ii} =

Fig. 1: The general circuit diagram